

BIBLIOGRAPHY

- Arnold, M. G., *Verilog Digital Computer Design*, Prentice Hall, Upper Saddle River, NJ, 1999.
- Bhasker, J., *VHDL Primer*, 3rd ed., Prentice Hall, Upper Saddle River, NJ, 1999.
- Breeding, K., *Digital Design Fundamentals*, 2nd ed., Prentice Hall, Upper Saddle River, NJ, 1992.
- Brown, S. and Vranesic, Z., *Fundamentals of Digital Logic with VHDL Design*, McGraw-Hill, New York, 2000.
- Burns, J., Within the 68020, *Electronics and Wireless World*, pp. 209–212, February 1985; pp. 103–106, March 1985.
- Ciletti, M. D., *Modeling, Synthesis, and Prototyping with the Verilog HDL*, Prentice Hall, Upper Saddle River, NJ, 1999.
- Daconta, M., *Java for C/C++ Programmers*, Wiley, Hoboken, NJ, 1996.
- Dewey, A., *Analysis and Design of Digital Systems with VHDL*, PWS Publishing, Boston, 1997.
- Feibus, M. and Slater, M., Pentium power, *PC Magazine*, April 27, 1993.
- Hall, D., *Microprocessors and Interfacing*, McGraw-Hill, New York, 1986.
- Hamacher, V. C., Vranesic, Z. G., and Zaky, S. G., *Computer Organization*, McGraw-Hill, New York, 1978; 2nd ed., 1984; 3rd ed., 1990.
- Hartman, B., 16-Bit 68000 Microprocessor concepts on 32-bit frontier, *MC 68000 Article Reprints*, Motorola, pp. 50–57, March 1981.
- Hayes, J., *Computer Architecture and Organization*, McGraw-Hill, New York, 1978.
- Hayes, J., *Digital System Design and Microprocessors*, McGraw-Hill, New York, 1984.
- Hayes, J., *Introduction to Digital Logic Design*, Addison-Wesley, Reading, MA, 1993.
- Hwang, K., and Briggs, F. A., *Computer Architecture and Parallel Processing*, McGraw-Hill, New York, 1984.
- Intel, *Intel Component Data Catalog*, Intel Corporation, Santa Clara, CA, 1979.
- Intel, *Intel 486 Microprocessor Family Programmer's Reference Manual*, Intel Corporation, Santa Clara, CA, 1992.
- Intel, *Intel 486 Microprocessor Hardware Reference Manual*, Intel Corporation, Santa Clara, CA, 1992.
- Intel, Marketing communications, *The Semiconductor Memory Book*, Wiley, Hoboken, NJ, 1978.
- Intel, *MCS-86 User's Manual*, Intel Corporation, Santa Clara, CA, 1982.
- Intel, *Memory Components Handbook*, Intel Corporation, Santa Clara, CA, 1982.
- Intel, *Microprocessors and Peripheral Handbook, Vol. 1, Microprocessors*, Intel Corporation, 1988.
- Intel, *Microprocessors and Peripheral Handbook, Vol. 2, Peripheral*, Intel Corporation, Santa Clara, CA, 1988.
- Intel, *Pentium Processor User's Manual*, Intel Corporation, Santa Clara, CA, 1993.
- Intel, *80386 Advance Information*, Intel Corporation, Santa Clara, CA, 1985.
- Intel, *80386 Hardware Reference Manual*, Intel Corporation, Santa Clara, CA, 1986.
- Intel, *80386 Programmer's Reference Manual*, Intel Corporation, Santa Clara, CA, 1986.

- Intel, *The 8086 Family User's Family*, Intel Corporation, Santa Clara, CA, 1979.
- Johnson, A comparison of MC68000 family processors, *BYTE*, pp. 205–218, September 1986.
- Katz, R., *Contemporary Logic Design*, Benjamin/Cummings, San Francisco, 1994.
- Lee, S., *Design of Computers and other Complex Digital Devices*, Prentice Hall, Upper Saddle River, NJ, 2000.
- Mano, M., *Computer Engineering*, Prentice Hall, Upper Saddle River, NJ, 1988.
- Mano, M., *Computer System Architecture*, Prentice Hall, Upper Saddle River, NJ, 1983.
- Mano, M., *Digital Design*, 2nd ed., Prentice Hall, Upper Saddle River, NJ, 1991; 3rd ed., 2002.
- Mano, M., and Kime, C., *Logic and Computer Design Fundamentals*, 2nd ed. updated, Prentice Hall, Upper Saddle River, NJ, 2001.
- Miller, M., Raskin, R., and Rupley, S., The pentium that stole christmas, *PC Magazine*, February 27, 1995.
- Motorola, *MC68000 16-Bit Microprocessor User's Manual*, Motorola Corporation, Schaumburg, IL, 1982.
- Motorola, *MC68000 Supplement Material (Technical Training)*, Motorola Corporation, Schaumburg, IL, 1982.
- Motorola, *MC68000 User's Manual*, Motorola Corporation, Schaumburg, IL, 1979.
- Motorola, *MC68020 Course Notes*, MTTA20 Rev. 2, Motorola Corporation Schaumburg, IL, July 1987.
- Motorola, *MC68020 User's Manual*, Motorola Corporation, Schaumburg, IL, 1985.
- Motorola, *MC68020/68030 Audio Course Notes*, Motorola Corporation, Schaumburg, IL, 1988.
- Motorola, *MC68040 User's Manual*, Motorola Corporation, Schaumburg, IL, 1989.
- Motorola, *Microprocessor Data Material*, Motorola Corporation, Schaumburg, IL, 1981.
- Motorola, *Power PC 601, RISC Microprocessor User's Manual*, Motorola Corporation, Schaumburg, IL, 1993.
- Motorola, *Technical Summary, 32-Bit Virtual Memory Microprocessor*, MC68020 BR243/D, Rev. 2, Motorola Corporation, Schaumburg, IL, 1987.
- Motorola, *16-Bit Microprocessor: MC68000 User's Manual*, 4th ed., Prentice Hall, Upper Saddle River, NJ, 1984.
- Motorola, *68020 User's Manual*, 2nd ed., MC68020 UM/AD Rev. 1, Prentice Hall, Upper Saddle River, NJ, 1984.
- National Semiconductor, *CMOS Logic Data Book*, National Semiconductor, Santa Clara, CA, 1988.
- National Semiconductor, *Fast® Advanced Schottky TTL Logic Data Book*, National Semiconductor, Santa Clara, CA, 1990.
- National Semiconductor, *LS/S/TTL Logic Data Book*, National Semiconductor, Santa Clara, CA, 1989.
- National Semiconductor, *Programmable Logic Devices Data Book and Design Guide*, National Semiconductor, Santa Clara, CA, 1989.
- Nelson, V. P., Nagle, H. T., Irwin, J. D., and Carroll, B. D., *Digital Logic Circuit Analysis and Design*, Prentice Hall, Upper Saddle River, NJ, 1995.
- Osborne, A., *An Introduction to Microprocessors*, Vol. 1, *Basic Concepts*, rev. ed., Osborne/McGraw-Hill, Berkeley, CA, 1980; 2nd ed., 1982.
- Palnitkar, S., *Verilog HDL: A Guide to Digital Design and Synthesis*, SunSoft Press, Division of Prentice Hall, Upper Saddle River, NJ, 1996.

- Pellerin, D., and Holley, M., *Digital Design Using ABEL*, Prentice Hall, Upper Saddle River, NJ, 1994.
- Rafiquzzaman, M., *Microprocessors and Microcomputer Development Systems: Designing Microprocessor-Based Systems*, Harper & Row, New York, 1984.
- Rafiquzzaman, M., *Microcomputer Theory and Applications with the INTEL SDK-85*, 2nd ed., Wiley, Hoboken, NJ, 1987.
- Rafiquzzaman, M., *Microprocessors—Theory and Applications—Intel and Motorola*, Prentice Hall, Upper Saddle River, NJ, 1992.
- Rafiquzzaman, M., and Chandra, R., *Modern Computer Architecture*, West / PWS, Boston, 1988.
- Rafiquzzaman, M., *Microprocessors and Microcomputer-Based System Design*, CRC Press, Boca Raton, FL, 1990; 2nd ed., 1995.
- Smith, D., and Franzon, P., *Verilog Styles for Synthesis of Digital Systems*, Prentice Hall, Upper Saddle River, NJ, 2000.
- Smith, J., and Weiss, S., Power PC 601 and Alpha 21064: a tale of two RISCs, *IEEE Computer*, June 1994.
- Tanenbaum, A. S., *Structured Computer Organization*, Prentice Hall, Upper Saddle River, NJ, 1984.
- Texas Instruments, *Linear Circuits Data Book*, Texas Instruments, Dallas, TX, 1990.
- Texas Instruments, *The TTL Data Book*, Vol. 1, Texas Instruments, Dallas, TX, 1984.
- Texas Instruments, *The TTL Data Book for Design Engineers*, 2nd ed., Texas Instruments, Dallas, TX, 1976.
- Tocci, R. J., and Widmer, N. S., *Digital Systems*, 7th ed., Prentice Hall, Upper Saddle River, NJ, 1998.
- Triebel, W., *The 80386 DX Microprocessor*, Prentice Hall, Upper Saddle River, NJ, 1992.
- Triebel, W., and Singh, A. *The 8086 Microprocessor*, Prentice Hall, Upper Saddle River, NJ, 1985.
- Van der Spiegel, J., *VHDL Tutorial*, Department of Electrical Engineering, University of Pennsylvania, Philadelphia, April 2002.
- Wakerly, J., *Digital Design Principles and Practices*, 3rd ed. updated, Prentice Hall, Upper Saddle River, NJ, 2001.
- White, R., *How Computers Work*, millennium ed., Que Corporation, 1999.
- www.activewin.com, Windows 2000, Active Windows, 2000.
- www.activewin.com, DVD FAQs, Active Windows, 2000.
- Zorpette, G., Microprocessors - The beauty of 32-bits, *IEEE Spectrum*, Vol. 22, No.9, pp. 65–71, September 1994.

