

## CREDITS

---

The following material is reprinted by permission of the sources listed below:

Intel Corporation: Figure 9.3, Figure 9.4, Figure 9.8, Figure 9.9, Figure 9.10, Figure 9.19, Figure 9.33, Figure 9.35, Appendix E, Appendix F. All mnemonics of Intel microprocessors are courtesy of Intel Corporation. The 80386 microprocessor referred to in the text as the i386™, the 80486 as the i486™, and the Pentium as the Pentium™, trademarks of Intel Corporation.

Motorola Corporation: Table 10-1, Table 10-2, Table 10-3, Table 10-13, Table 10-15, Table 10-16, Table 10-19, Table 10-20, Table 10-21, Table 10-23, Figure 10.1, Figure 10.2, Figure 10.3, Figure 10.6, Figure 10.7, Figure 10.7, Figure 10.13, Figure 10.17, Figure 10.19, Figure 10.28, Figure 10.28, Figure 10.32, Figure 10.33, Figure 10.34, Appendix C, Appendix D. All mnemonics of Motorola microprocessors are courtesy of Motorola Corporation.

Rafiquzzaman, M. and Chandra, Rajan, "Modern Computer Architecture," 1988, West/PWS Publishing/Rafi Systems Inc. used with permission; Figure 4.16, Figure 4.32, Figure 4.34, Figure 4.35, Figure 4.40, Figure 4.41, Figure 5.40, Figure 5.41, Figure 5.42, Figure 5.43, Figure 5.44, Figure 7.7, Figure 7.9, Figure 7.13, Figure 7.15, Figure 7.16, Figure 7.17, Figure 7.18, Figure 7.28, Figure 7.29, Figure 7.34, Figure 7.38, pages 169-174, pages 186-188, pages 306-326, pages 220-232, pages 242-267.1

